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# XA-9992 PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Masanori OWAKI et al.

Appln. No.: 10/714,983

Group Art Unit: 2811

Filed: November 18, 2003

For: MULTI-CHIP MODULE

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

ATTN: OIPE, Filing Receipt Corrections

Sir:

It is respectfully requested that the official filing receipt be corrected as indicated on the attached copy. See the accompanying supporting documentation.

Respectfully submitted,

MWS:lmb

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May 3, 2005

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**CONFIRMATION NO. 3658 UPDATED FILING RECEIPT** 

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Date Mailed: 03/30/2004

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# Applicant(s)

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#### **Assignment For Published Patent Application**

Renesas Technology Corp.; Shinko Electric Industries Co., Ltd.;

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Title

Multi-chip module

**Preliminary Class** 

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